



October 19th, 2009 – Ecosystem Quote Sheet

XILINX AND ARM ANNOUNCE DEVELOPMENT COLLABORATION

Agreement sets the foundation for industry's next-generation programmable platforms

“Cadence® and ARM have a long history of close collaboration to ensure that ARM IP and Cadence’s design and verification solutions are optimized for the successful development of ARM core-based SoCs. Today’s announcement from Xilinx and ARM to collaborate on new programmable platforms and expand AMBA® brings a much-needed standard interconnect to the FPGA market. It also provides the opportunity to apply our proven Enterprise Verification solution with Cadence Incisive® products, including AMBA verification IP and the Open Verification Methodology (OVM), to bring productivity and predictability to the increasingly complex verification challenge faced by FPGA customers.”

- Michał Siwiński, group director of Front End marketing
Cadence Design Systems, Inc.

“Xilinx’s adoption of the AMBA interconnect technology not only aligns well with our own product direction, but matches what many of our SoC customers are already asking for. Adopting a broad industry standard like this will dramatically simplify SoC design and integration for FPGA customers and the IP developer community. CAST looks forward to support this initiative with our broad portfolio of proven IP cores.”

- Newton Abdalla, Vice President of IP
CAST, Inc.

“As a member of the ARM Connected Community, Denali Software provides design and verification IP for the ARM architecture that enables customers to speed chip development and reduce design verification risks. The alliance between Xilinx and ARM to create an ARM-based FPGA platform and Xilinx’s decision to standardize on AMBA allows Denali to extend our robust design and verification offerings to better support the needs of these SoC designers.”

- Sanjiv Kumar, director, Verification IP products
Denali Software, Inc.

“Standard, high performance on-chip fabrics are critical to SoC designs as they provide a plug-and-play model for connecting reusable IP blocks. Mentor Graphics is excited to expand our existing partnership with Xilinx and ARM to enable this new world of FPGA-based SoCs with ARM AMBA-centric verification solutions that will help speed these systems to market.”

- Simon Bloch, VP and General Manager
Mentor Graphics Corp.

“Northwest Logic’s IP Cores already support the ARM AMBA interconnect standards, so as a leading provider of DDR3/2 SDRAM Controller and PCI Express2.0/1.1 IP Cores for Xilinx FPGAs, we are

excited that Xilinx is adopting the industry's leading processor and interconnect technology. We will ensure that the full range of our high-performance, high-quality IP cores support Xilinx's next generation of embedded products.”

- Brian Daellenbach, President
Northwest Logic

“Omiino and Xilinx have collaborated closely in successfully delivering innovative Packet Optical Transport Network (P-OTN) products for our mutual customers. Xilinx's actions to standardize on the AMBA interconnect will accelerate the development and verification of our Virtual ASSP solutions, benefiting our customers with a shorter time-to-market and faster integration.”

- Gary Hamilton, CEO
OMIINO Ltd.

“Sarance develops Ethernet, Interlaken and other packet processing IP cores used by many Tier-1 data communications companies around the world and we have worked closely with Xilinx to deliver best-in-class support for Xilinx FPGAs. The collaborative work being done by Xilinx and ARM to extend an industry standard interconnect like AMBA to FPGAs will allow customers to more effectively integrate and reuse Sarance IP into their FPGA SoC designs.”

- Farhad Shafai, Vice President of R&D
Sarance Technologies Inc.

“By adopting the ARM® Cortex™ processor IP and AMBA® interconnect, Xilinx enables its customers to take advantage of the robust suite of solutions available from ARM ecosystem partners. Synopsys DesignWare® IP solutions for AMBA® include a comprehensive suite of AMBA® bus fabrics, verification IP and connectivity cores with AMBA® interfaces. In addition, we continue to collaborate with ARM and Xilinx on next generation design tools and connecting to the Confirma™ Rapid Prototyping Platform.”

- John Koeter, Vice President of Marketing for Synopsys's Solutions Group
Synopsys, Inc.

“As a very active Xilinx Alliance Member with IP in the embedded space, we see standardization on AMBA as an important enabler for Xilinx leadership in processor based solutions. We also see this initiative as a way to better enable customers to seamlessly integrate our IP into their products and look forward to supporting this interface on our logicBRICKS™ IP portfolio.”

- Davor Kovacec, CEO
Xylon d.o.o.

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